

# Compact noise modelling for common double-gate metal–oxide–semiconductor field-effect transistor adapted to gate-oxide-thickness asymmetry

ISSN 1751-858X

Received on 4th February 2015

Accepted on 12th July 2015

doi: 10.1049/iet-cds.2015.0128

www.ietdl.org

Neha Sharan ✉, Santanu Mahapatra

Nano Scale Device Research Laboratory, Department of Electronic Systems Engineering, Indian Institute of Science, Bangalore 560012, India

✉ E-mail: nehasharan11@yahoo.com

**Abstract:** On the basis of the quasi-linear relationship between the surface potentials of a common double-gate metal–oxide–semiconductor field-effect transistor, a compact noise model, which is adapted to gate-oxide-thickness asymmetry, is proposed. The proposed model includes a physics-based thermal and flicker noise model. The effect of the lateral and vertical electric fields on the mobility degradation has also been taken into account for accurate noise prediction in short-channel devices. The thermal noise model is compared with the technology computer aided design (TCAD) simulation data and good agreement is observed. The proposed noise model appears to be efficient for analogue circuit simulation.

## 1 Introduction

Common double-gate metal–oxide–semiconductor field-effect transistor (CDG-MOSFET) has emerged as one of the most viable option to replace the bulk MOSFET in sub-32 nm technology nodes [1, 2] due to its improved electrostatic integrity and better gate control over the channel. For successful utilisation of these devices in radio frequency (RF) analog circuit applications efficient compact noise models are required to predict the noise characteristics accurately at both the low- and high-frequency domain. Noise at low frequency is dominated by the flicker noise ( $1/f$ ) and at high frequency by the thermal noise. Although flicker noise is predominant only at low frequency, in some circuits such as voltage controlled oscillator, where upconversion is performed, it has an impact also on the high-frequency device behaviour. There are two distinct models to describe the origin of flicker noise: carrier number fluctuation and mobility fluctuation [3, 4]. Carrier number fluctuation in the channel as proposed by McWorther [5] is due to the random trapping and de-trapping of charge carriers in the oxide traps at the Si–SiO<sub>2</sub> interface. Mobility fluctuation as proposed by Hooge law [6] is attributed to the phonon scattering. It is observed that at bias conditions below the threshold voltage, flicker noise is dominated by the mobility fluctuation and above threshold it is dominated by carrier number fluctuation [7]. Thus, in this work, we propose a unified flicker noise model based on both these phenomenon. Thermal noise on other hand is generated due to random Brownian motion of electrons which leads to heating of the charge carriers in channel. Thermal noise is best described by Klaassen–Prins equation [3] and therefore, in this paper, we have used this equation to model thermal noise. Moreover, in order to make the model suitable for submicron devices, it is important to consider the field-dependent electron mobility degradation on noise performance [8, 9]. We have incorporated the effect of lateral and perpendicular electric field through mobility and conductance expression. The proposed noise model is based on our recent work on small-channel length CDG-MOSFET [10] and found to be working efficiently in all operating regimes. Due to the lack of experimental results thermal noise model has been verified against TCAD [11] simulation results. Since TCAD does not provide flicker noise simulation model, we cannot validate the proposed flicker noise model. However, it is observed that the characteristics and the range of

flicker noise predicted from the model matches closely with experimental results for the symmetric devices.

## 2 Model development

The conventions used in this paper are:  $t_{ox_1(2)}$  is the oxide thickness of first(second) gate,  $t_{si}$  is the thickness of the silicon body,  $C_{ox_1(2)}$  is the oxide capacitance per unit area of first (second) gate defined as  $\epsilon_{ox}/t_{ox_1(2)}$ ,  $\epsilon_{si}$ ,  $\epsilon_{ox}$  are the permittivities of Si and SiO<sub>2</sub>, respectively,  $q$  is the elementary charge,  $\beta$  is the inverse thermal voltage,  $n_i$  is the intrinsic carrier density,  $B = 2qn_i/\beta\epsilon_{si}$ ,  $L$  is the channel length,  $W$  is the channel width,  $\psi_{1(2)}$  is the Si/SiO<sub>2</sub> surface potentials at first (second) gate,  $V$  is the electron quasi-Fermi potential (channel potential) and  $\mu$  is the low-field electron mobility. The effective gate voltage is defined as  $V_{gs_1(2)} = V_{gs_1(2)_{applied}} - \delta\phi_{1(2)}$ , where  $V_{gs_1(2)_{applied}}$  is the voltage applied at gate terminals and  $\delta\phi_{1(2)}$  is the work function difference of the gate material. The inversion charge density at any point along the channel is denoted by  $Q_i$ , which is the sum of two components  $Q_{i_1}$  and  $Q_{i_2}$  expressed as  $Q_i(2) = C_{ox_1(2)}(V_{gs_1(2)} - \psi_{1(2)})$ . In the following discussions, any variable with subscript ‘s’ refers to its value at source end and ‘d’ refers to its value at drain end. The variable  $x$  denotes the direction along the thickness of the channel with  $x = \pm t_{si}/2$  represent the front and back Si/SiO<sub>2</sub> interface. The variable  $y$  represents the direction along the length of the channel while  $y = 0$  and  $L$  represents the source end and the drain end of the channel, respectively.

Existing compact models for CDG-MOSFETs [12–15] are based on the fundamental assumption of having symmetric gate-oxide thickness, as it greatly simplifies the model development process. Recently, we have demonstrated that the models could be generalised by considering the asymmetry between gate-oxide thickness as there could be a possibility of having asymmetry between the gate-oxide thickness due to process variations and uncertainties [10]. In that work, we however considered the mobility degradation in short-channel MOSFETs only due to the lateral electric field. To include the vertical field effect, in this work we use the following expressions [16]

$$\mu_{eff} = \mu \cdot \mu_{pa} \cdot \mu_{pe} \quad (1)$$

where

$$\mu_{pa} = \frac{1}{[1 + (\mu/v_{sat})(dV/dy)^2]^{1/2}} \quad (2)$$

$$\mu_{pe} = \frac{1}{[1 + (\mu/E_f)(dV/dx)]^{1/2}} \quad (3)$$

Evaluating (1) and (3) gives

$$\mu_{eff} = \mu \cdot \mu_{pa} \cdot \frac{\mu_{pe1} + \mu_{pe2}}{av} \quad (4)$$

where  $av$  and  $E_f$  are model parameters, and  $\mu_{pe1}$  and  $\mu_{pe2}$  are obtained by evaluating (3) across the front and back surfaces as

$$\mu_{pe1} = \frac{1}{[1 + (\mu/E_f)(V_{sat1}/t_{ox1})]^{1/2}},$$

$$\mu_{pe2} = \frac{1}{[1 + (\mu/E_f)(V_{sat2}/t_{ox2})]^{1/2}}$$

here  $V_{sat1(2)}$  is the respective saturation voltage across front and back surfaces, expression of which are given in (Fig. 1). Fig. 1 validates that the accuracy of improved model with incorporation of vertical field effect dependency.

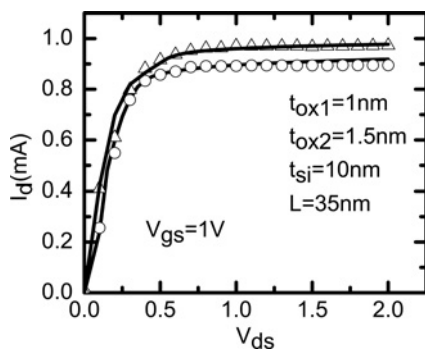
The channel noise comprises of thermal noise and flicker noise. For modelling noise in CDG-MOSFET we first calculate the respective spectral density.

### 2.1 Thermal noise

As proposed in [17] the conventional Klaassen–Prin equation does not accurately account for the impact of velocity saturation and hence an improved Klaassen–Prin equation was suggested which works correctly in the presence of the velocity saturation. The drain current spectral density following this improved Klaassen–Prins equation is given by [17]

$$S_{id} = \frac{4kT}{I_{ds}L_c^2} \int_{V_s}^{V_d} g_c^2(V) \left[ 1 + \left( \frac{\mu}{v_{sat}} \frac{dV}{dy} \right)^2 \right] dV \quad (5)$$

$$L_c = \frac{L \int_{V_s}^{V_d} g_c(V) dV}{\int_{V_s}^{V_d} g(V) dV} g_c$$



**Fig. 1** Drain current plot with variation in drain voltage as predicted by TCAD simulation (symbol) and proposed model (line). The triangle denotes characteristics including only mobility degradation due to lateral electric field (both in TCAD and model). The circle denotes characteristic, including mobility dependence on both lateral and vertical fields

is the modified channel length and

$$g_c(V) = g(V) \left[ \frac{g_o(V)}{g(V)} \right]^2$$

is the modified channel conductance. The local conductance  $g(V)$  with incorporation of velocity saturation could be expressed as

$$g(V) = g_o(V) \left[ 1 + \left( \frac{\mu}{v_{sat}} \frac{dV}{dy} \right)^2 \right]^{1/2}$$

with  $g_o(V) = \mu \cdot \mu_{pe} W Q_i$ .

Solving (5) results in an equation in terms of inversion charge across the channel and since in asymmetric CDG-MOSFET we have two surfaces with different potentials solving (5) directly is not trivial. Therefore, as proposed in [18], we linearise the potential across one surface in terms of the other to obtain the total inversion charge as

$$Q_i = Q_{i1} + m_1 Q_{i2} + c_1 \quad (6)$$

which yields

$$\frac{dV}{dQ_{i1}} = -\frac{1}{C_{ox1}} - \frac{(2nQ_{i1} + p_1)}{\beta(nQ_{i1}^2 + p_1Q_{i1} + q_1)} \quad (7)$$

$$\frac{dQ_{i1}}{dy} = \frac{1}{2\alpha_1 L Q_{i1} + \gamma_1} \quad (8)$$

Here  $n, p_1, q_1, \alpha_1$  and  $\gamma_1$  are the linearisation coefficients, expression of which could be found in [10, 18]. Finally on evaluating (5) using the above linearisation coefficients, we obtain

$$S_{id} = CC \int_{V_s}^{V_d} Q_i^2 \left[ 1 + \left\{ \frac{(-1/C_{ox1} - (2nQ_{i1} + p_1)\beta^{-1}/(nQ_{i1}^2 + p_1Q_{i1} + q_1))}{E_{sat}(2\alpha_1 Q_{i1} L + \gamma_1 L)} \right\}^2 \right] dV \quad (9)$$

where  $CC = 4kT(\mu \cdot \mu_{pe} W)^2 / I_{ds} L_c^2$  and  $E_{sat} = v_{sat}/\mu$ . Direct solution of (9) is not possible and therefore, based on the observation made through TCAD simulations, we approximate

$$-\frac{1}{C_{ox1}} - \frac{1}{\beta} \frac{(2nQ_{i1} + p_1)}{(nQ_{i1}^2 + p_1Q_{i1} + q_1)} \simeq f(Q_{i1}) \quad (10)$$

where  $f(Q_{i1}) = A_{11}Q_{i1} + B_{11}$  with  $A_{11} = \frac{f(Q_{i1s}) - f(Q_{i11d})}{Q_{i1s} - Q_{i11d}}$ ,  $B_{11} = \frac{f(Q_{i11d})Q_{i1s} - f(Q_{i1s})Q_{i11d}}{Q_{i1s} - Q_{i11d}}$ ,  $Q_{i11d} = m_q(Q_{i1s} + Q_{i1d})$ , and  $m_q$  is the model parameter. Using this approximation we obtain

$$S_{id} = CC \int_{Q_{i1s}}^{Q_{i1d}} Q_{i1} f(Q_{i1}) \left[ 1 + \left\{ \frac{(A_{11}Q_{i1} + B_{11})}{E_{sat}(2\alpha_1 Q_{i1} L + \gamma_1 L)} \right\}^2 \right] dQ_{i1} \quad (11)$$

Evaluating which results in

$$I1_1 = \frac{p_1 CC}{n} \left( \frac{\delta_1}{\beta C_{ox1} n} - \frac{p_1 (nm_1^2 + 2nm_1 + n)}{C_{ox1} n^2} \right) \quad (12)$$

$$I1_2 = CC \left( \frac{\beta q_1 M_1^2 + C_{ox1} p_1 M_1^2 + \beta c_1 \delta_2 + 4c_1 C_{ox1} n M_1}{\beta C_{ox1} n} \right) \quad (13)$$

$$I1_3 = \frac{CC q_1 (\beta n m_1^2 + 2\beta n m_1 + \beta n)}{\beta C_{ox1} n^2} \quad (14)$$

$$I1_4 = CC Q_i^2 \left( \frac{\delta}{2\beta C_{ox1} n} + \frac{p_1 (\beta n m_1^2 + 2\beta n m_1 + \beta n)}{2\beta C_{ox1} n^2} \right) \quad (15)$$

$$I1_5 = \frac{CC \cdot \log(Y_1) \{p_1 \vartheta_1 - 2nq_1 M_1^2 + c_1 n \vartheta_2\}}{2\beta n^2} \quad (16)$$

$$I1_6 = \frac{CC \cdot \log(Y_2) (\vartheta_3 - 2c_1 n (c_1 n - \zeta - m_1 \zeta) + 2nq_1 M_1^2)}{2\beta n^2} \quad (17)$$

$$I1_7 = \frac{CC Q_i^3 (\beta n m_1^2 + 2\beta n m_1 + \beta n)}{3\beta C_{ox1} n} \quad (18)$$

$$I1 = Q_i (I1_1 - I1_2 + I1_3) + I1_4 - I1_5 + I1_6 \quad (19)$$

where

$$\begin{aligned} \delta_1 &= C_{ox1} (2n + 2m_1^2 n + 4m_1 n) \\ &+ \beta (p_1 + m_1^2 p_1 + 2c_1 n + 2m_1 p_1 + 2c_1 m_1 n), \\ \delta_2 &= c_1 n + 2p_1 + 2m_1 p_1, \quad M_1 = 1 + m_1, \end{aligned}$$

$$Y_1 = p_1 \zeta + \zeta^2 + 2Q_i n \zeta$$

$$Y_2 = p_1 \zeta - \zeta^2 + 2Q_i n \zeta$$

$$\zeta = \sqrt{p_1^2 - 4nq_1}$$

$$\vartheta_1 = p_1 + m_1^2 p_1 + 2m_1 p_1 + \zeta + 2m_1 \zeta + m_1^2 \zeta - 2c_1 m_1 n - 2c_1 n$$

$$\vartheta_2 = 2c_1 n - 2\zeta - 2m_1 \zeta$$

$$\begin{aligned} \vartheta_3 &= -p_1^2 - m_1^2 p_1^2 - 2m_1 p_1^2 + 2m_1 p_1 \zeta + p_1 \zeta + m_1^2 p_1 \zeta + 2c_1 m_1 n p_1 \\ &+ 2c_1 n p_1 \end{aligned}$$

$$I2_1 = \frac{\gamma_1 CC \mu^2 A_{11}^2}{\alpha_1} \left( \frac{e_1}{4L_e^2 \alpha_1^2 v_{sat}^2} - \frac{\gamma_1 (A_{11} (1 + m_1)^2)}{4L_e^2 \alpha_1^3 v_{sat}^2} \right) \quad (20)$$

$$I2_2 = \frac{CC (A_{11}^3 c_1^2 \mu^2 + 3A_{11} B_{11} c_1 \mu^2 e_2)}{4L_e^2 \alpha_1^2 v_{sat}^2} \quad (21)$$

$$I2_3 = \frac{CC \gamma_1^3 A_{11}^3 \mu^2 (1 + m_1)^2}{16L_e^2 \alpha_1^4 v_{sat}^2} \quad (22)$$

$$I2_4 = \frac{CC (2c_1 A_{11}^3 \mu^2 (1 + m_1) + 3B_{11} A_{11}^2 \mu^2 (1 + m_1)^2)}{4L_e^2 \alpha_1^2 v_{sat}^2} \quad (23)$$

$$I2_5 = \frac{\gamma_1 CC A_{11}^3 \mu^2 (1 + m_1)^2}{4L_e^2 \alpha_1^3 v_{sat}^2} \quad (24)$$

$$I2_6 = \frac{CC A_{11} B_{11} c_1 \mu^2 e_3}{4L_e^2 \alpha_1^2 v_{sat}^2} \quad (25)$$

$$I2_7 = \frac{CC (A_{11}^3 c_1^2 \mu^2 + 3A_{11} B_{11} \mu^2 e_4)}{8L_e^2 \alpha_1^2 v_{sat}^2} \quad (26)$$

$$I2_8 = CC 4A_{11}^3 c_1 \alpha_1 \gamma_1^3 \mu^2 (\alpha_1^2 c_1 + \gamma_1 (1 + m_1)) \quad (27)$$

$$I2_9 = CC A_{11}^3 \gamma_1^5 \mu^2 (1 + m_1)^2 - 24CC A_{11}^2 B_{11} \alpha_1^3 c_1^2 \gamma_1^2 \mu^2 \quad (28)$$

$$I2_{10} = 6CC A_{11}^2 B_{11} \mu^2 (1 + m_1)^2 \left( \frac{4\alpha_1^2 c_1 \gamma_1^3}{(1 + m_1)} - 6\alpha_1 \gamma_1^4 \right) \quad (29)$$

$$I2_{11} = 48CC A_{11} B_{11}^2 \alpha_1^3 c_1 \gamma_1 \mu^2 (\alpha_1 c_1 - \gamma_1 (1 + m_1)) \quad (30)$$

$$I2_{12} = 12CC A_{11} B_{11}^2 \alpha_1^2 \gamma_1^3 \mu^2 (1 + m_1)^2 - 32CC B_{11}^3 \alpha_1^5 c_1^2 \mu^2 \quad (31)$$

$$I2_{13} = 8CC B_{11}^3 (4\alpha_1^4 c_1 \gamma_1 \mu^2 (1 + m_1) - \alpha_1^3 \gamma_1^2 \mu^2 (1 + m_1)^2) \quad (32)$$

$$I2_{14} = \frac{CC A_{11}^3 \mu^2 (1 + m_1)^2}{16L_e^2 \alpha_1^2 v_{sat}^2} \quad (33)$$

$$I2_{15} = 12CC A_{11}^3 \alpha_1^2 c_1^2 \gamma_1^2 \mu^2 - 16CC A_{11}^3 \alpha_1 c_1 \gamma_1^3 \mu^2 (1 + m_1) \quad (34)$$

$$I2_{16} = 5CC A_{11}^3 \gamma_1^4 \mu^2 (1 + m_1)^2 - 48CC A_{11}^2 B_{11} \alpha_1^3 c_1^2 \gamma_1 \mu^2 \quad (35)$$

$$I2_{17} = 24CC A_{11}^2 \mu^2 (1 + m_1)^2 \left( \frac{3\alpha_1^2 c_1 \gamma_1^2}{(1 + m_1)} - B_{11} \alpha_1 \gamma_1^3 \right) \quad (36)$$

$$I2_{18} = 48CC A_{11} B_{11}^2 \mu^2 (\alpha_1^4 c_1^2 - 2\alpha_1^3 c_1 \gamma_1 (1 + m_1)) \quad (37)$$

$$I2_{19} = 36CC A_{11} B_{11}^2 \alpha_1^2 \gamma_1^2 \mu^2 (1 + m_1)^2 \quad (38)$$

$$I2_{20} = 16CC B_{11}^3 (2\alpha_1^4 c_1 \mu^2 (1 + m_1) - \alpha_1^3 \gamma_1 \mu^2 (1 + m_1)^2) \quad (39)$$

$$\begin{aligned} I2 &= Q_i \left\{ \frac{\gamma_1 (I2_1 - I2_2 + I2_3)}{\alpha_1} - \frac{\gamma_1^2 (I2_4 + I2_5)}{4\alpha_1^2} \right\} \\ &+ Q_i I2_6 + Q_i^2 \left\{ \frac{\gamma_1}{2\alpha_1} (I2_4 + I2_5) - I2_7 + \frac{\gamma_1 I2_5}{8\alpha_1} \right\} \\ &+ Q_i^3 \left\{ \frac{I2_4}{3} - \frac{I2_5}{3} \right\} + Q_i^4 I2_{14} \\ &+ \frac{I2_8 + I2_9 + I2_{10} + I2_{11} + I2_{12} + I2_{13}}{4\alpha_1 (32Q_i L_e^2 \alpha_1^6 v_{sat}^2 + 16\gamma_1 L_e^2 \alpha_1^5 v_{sat}^2)} \\ &+ \frac{\log(\gamma_1 + 2Q_i \alpha_1) \{I2_{15} + I2_{16} + I2_{17}\}}{64L_e^2 \alpha_1^6 v_{sat}^2} \\ &+ \frac{\log(\gamma_1 + 2Q_i \alpha_1) \{I2_{18} + I2_{19} + I2_{20}\}}{64L_e^2 \alpha_1^6 v_{sat}^2} \quad (40) \end{aligned}$$

where  $Q_1 = (2A_{11}c_1(1 + m_1) + 3B_{11}(1 + m_1)^2)$ ,  $e_2 = 2m_1A_{11} + 2A_{11} + B_{11}m_1^2 + 2B_{11}m_1 + B_{11}$ ,  $e_3 = 3A_{11}c_1 + 6B_{11}m_1 + 6B_{11} + B_{11}^2m_1^2 + 2B_{11}^2m_1 + B_{11}^2$ ,  $Q_4 = (2A_{11}c_1m_1 + 2A_{11}c_1 + B_{11}(m_1 + 1)^2)$  and finally

$$S_{id} = I1 + I2 \quad (41)$$

The induced gate current spectral density following the Klaassen–Prins

equation [17] is

$$S_{ig} = CC_1 \int_{V_s}^{V_d} g_c^2(V) \left( \underbrace{\int g_c(V') Q G dV'}_{PI} \right)^2 dV \quad (42)$$

where  $CC_1 = 4kT\omega^2 W^2 / I_d^2 L_c^2$  and  $QG = [Q_i(V') - Q_i(V)]$   
The inner integral can be written as

$$PI = \int \mu \cdot \mu_{pe} W Q_i \left[ 1 + \left( \frac{\mu}{v_{sat}} \frac{dV'}{dy} \right)^2 \right]^{1/2} Q G dV' \quad (43)$$

which yields

$$PI = \int_{V_s}^{V_d} \mu \cdot \mu_{pe} W Q_i \chi \{Q_i(V') - Q_i(V)\} dV' \quad (44)$$

$$\text{where } \chi = \left[ 1 + \left( \frac{\mu}{v_{sat}} \frac{(A_{11} Q'_i + B_{11})}{(2\alpha_1 L Q_i + \gamma_1 L)} \right)^2 \right]^{1/2}$$

Equation (44) can be written as

$$PI = \int_{V_s}^{V_d} \mu \cdot \mu_{pe} W Q_i (\chi Q_i(V') - \chi Q_i(V)) dV' \quad (45)$$

This results in a solution of the form

$$PI = \mu \cdot \mu_{pe} W (N_1 - Q_i N_2) \quad (46)$$

Using this in (42), we obtain

$$S_{ig} = CC_2 \int_{V_s}^{V_d} \left[ Q_i^2 + \left( \frac{\mu Q_i}{v_{sat}} \frac{dV}{dy} \right)^2 \right] (N_1 - Q_i N_2)^2 dV \quad (47)$$

where  $CC_2 = CC_1 \mu \cdot \mu_{pe} W$  and finally we obtain

$$S_{ig} = CC_2 (T_{e1} + T_{e2}) \quad (48)$$

where

$$T_{e1} = \int_{V_s}^{V_d} Q_i^2 (N_1 - Q_i N_2)^2 dV$$

$$T_{e2} = \int_{V_s}^{V_d} \left( \frac{\mu Q_i^2 (A_{11} Q_i + B_{11})}{L v_{sat} (2\alpha_1 Q_i + \gamma_1)} \right)^2 (N_1 - Q_i N_2)^2 dV$$

The cross-correlation spectral density is given by [17, 19]

$$S_{igid^*} = -jCC_3 \int_{V_s}^{V_d} g_c^2(V) \left( \underbrace{\int g_c(V') Q G dV'}_{PI} \right) dV \quad (49)$$

where  $CC_3 = 4kT\omega W / I_d^3 L_c^2$ .

Evaluating the inner integral in the same as in (43) gives

$$S_{igid^*} = -jCC_4 \int_{V_s}^{V_d} Q_i^2 (N_1 - Q_i N_2) (A_{11} Q_i + B_{11}) dQ_i \quad (50)$$

where  $CC_4 = CC_3 [\mu \cdot \mu_{pe} W]^3$ .

The final analytical solution for  $S_{ig}$  and  $S_{igid}$  is too long to include in the manuscript.

## 2.2 Flicker noise

Flicker noise is generally observed at low frequency and is commonly termed as  $1/f$  noise. Generally, the origin of flicker noise is attributed to the following theories:

**2.2.1 Carrier number fluctuation theory:** According to the theory proposed by McWorther's the origin of flicker noise is due to the tunnelling of charge carriers in traps located in gate dielectric [20]. The fluctuation in charge carriers leads to a fluctuation in surface potential and hence to channel carrier density [20]. Every single trap leads to a Lorentzian noise power spectrum and these Lorentzian spectra add up to give a  $1/f$  spectrum.

**2.2.2 Mobility fluctuation theory:** On the basis of Hooge's theory, the flicker noise is attributed to bulk mobility fluctuations caused by phonon scattering [20]. However, it is observed through experimental results that the origin of flicker noise has a much more complicated dependence on structural parameters and bias condition than suggested by the above two theories. Therefore, a unified theory that incorporates both carrier number fluctuation and mobility fluctuation was proposed [3] to explain flicker noise. Since both the number fluctuation and mobility fluctuation have the same origin and therefore, they are correlated. According to this unified correlated model we have [20]

$$\frac{\delta I_{ds}}{I_{ds}} = - \left[ \frac{1}{\Delta N_{inv}} \frac{\delta \Delta N_{inv}}{\delta \Delta N_i} \pm \frac{1}{\mu_{eff}} \frac{\delta \mu_{eff}}{\delta \Delta N_i} \right] \delta \Delta N_i \quad (51)$$

From [20–22], we have

$$\frac{\delta \Delta N_{inv}}{\delta \Delta N_i} = \frac{Q_i}{Q_i + C_{ox} U_T}$$

$$\frac{\delta \mu_{eff}}{\delta \Delta N_i} = - \frac{\alpha \mu_{eff}^2}{W \Delta x}$$

Following the same evaluation as in [22], we obtain:

$$S_{\Delta I_d}(x, f) = \left( \frac{I_d}{W} \right)^2 \left[ \frac{q}{Q_i + C_{ox} U_T} + \alpha \mu \right]^2 N_i \frac{kTW}{\eta f} \quad (52)$$

Here, the expression for  $\alpha$  is taken to be the same as in [22]. The drain current spectral density is given by

$$S_{id} = \frac{1}{I_d L_c^2} \int_{V_s}^{V_d} g_o(V) \left[ 1 + \left( \frac{\mu}{v_{sat}} \frac{dV}{dy} \right)^2 \right]^2 S_{\Delta I_d} dV \quad (53)$$

Evaluating this with (6–8), we get the flicker noise as

$$S_{id} = FC_1 \int_{Q_{is}}^{Q_{id}} Q_i \left[ 1 + \left( \frac{\mu}{v_{sat}} \frac{(A_{11} Q_i + B_{11})}{(2\alpha_1 Q_i + \gamma_1)} \right)^2 \right] \quad (54)$$

$$\left[ \frac{q}{Q_i + C_{ox} U_T} + \alpha \mu \right]^2 (A_{11} Q_i + B_{11}) dQ_i$$

where  $FC_1 = I_{ds} N_i kT \mu / L_c^2 \eta f$ .

The final analytical solution for flicker noise is also too long to include in the manuscript.

### 3 Results and discussions

We have validated our model against the two-dimensional TCAD simulation results [11] for devices having gate-oxide thickness asymmetry:  $t_{ox1} = 1 \text{ nm}$ ,  $t_{ox2} = 1.5 \text{ nm}$ ,  $t_{si} = 10 \text{ nm}$ ,  $L = 35 \text{ nm}$ ,  $W = 1 \mu\text{m}$ . The values of saturation velocity  $v_{sat}$  and saturation voltages  $V_{sat1}$ ,  $V_{sat2}$  are taken to be the same as in [10]. The values of various other model parameters is taken as  $E_f = 6.48 \times 10^{-6} \text{ V/m}$ ,  $N_t = 10^{23} \text{ cm}^{-3} \text{ eV}^{-1}$ ,  $\eta = 10^{10} \text{ m}^{-1}$ ,  $av = 0.22$ ,  $m_q = 0.216$ .

Fig. 2 shows the thermal noise drain current spectral density for a sweep of gate voltage,  $V_{gs}$  from 0 to 2 V predicted from TCAD

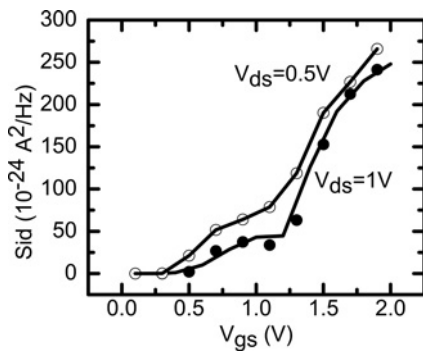


Fig. 2 Thermal noise drain current spectral density plot with variation in gate voltage for frequency  $f = 5 \text{ GHz}$  predicted from TCAD simulation (symbol) and proposed model (line)

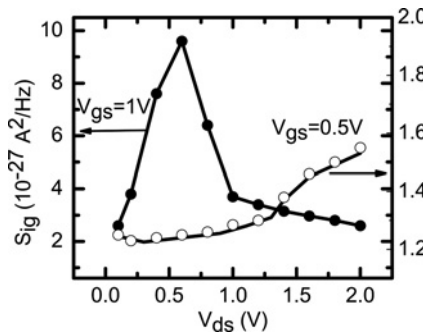


Fig. 3 Thermal noise gate current spectral density plot with variation in drain voltage at frequency  $f = 5 \text{ GHz}$  predicted from TCAD simulation (symbol) and proposed model (line)

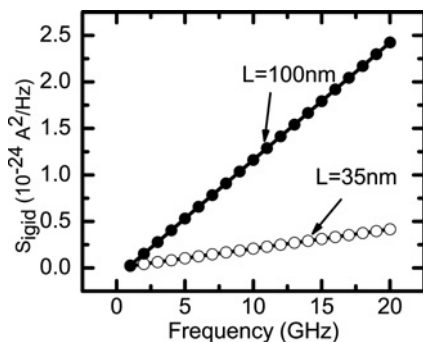


Fig. 4 Imaginary part of cross-correlation spectral density plot with variation frequency at  $V_{gs} = 1 \text{ V}$  and  $V_{ds} = 1 \text{ V}$  predicted from TCAD simulation (symbol) and proposed model (line)

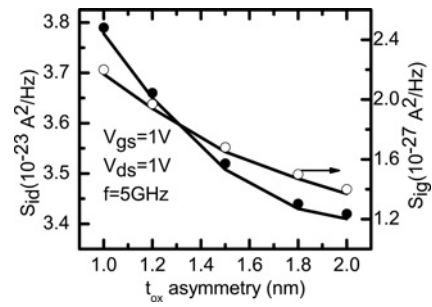


Fig. 5 Thermal noise drain current spectral density plot with variation in oxide thickness asymmetry at frequency  $f = 5 \text{ GHz}$ ,  $V_{gs} = 1 \text{ V}$  and  $V_{ds} = 1 \text{ V}$  predicted from TCAD simulation (symbol) and proposed model (line)

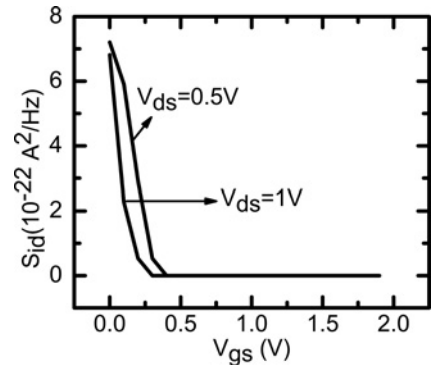


Fig. 6 Flicker noise drain current spectral density plot with variation in gate voltage at frequency,  $f = 5 \text{ GHz}$  predicted from proposed model

simulation and proposed model. Fig. 3 shows the thermal noise gate current spectral density for a sweep of drain voltage  $V_{ds}$  from 0 to 2 V predicted from TCAD simulation and proposed model. Fig. 4 shows the cross-correlation spectral density for a sweep of frequency from 1 to 20 GHz at  $V_{gs} = 1 \text{ V}$  and  $V_{ds} = 1 \text{ V}$  for two devices having  $L = 35$  and  $100 \text{ nm}$  predicted from TCAD simulation and the proposed model. It is observed that for all plots the results obtained from TCAD simulation and the proposed model matches accurately. Our model works very well for varied range of bias conditions and operating frequency.

Fig. 5 shows the impact of asymmetry in oxide thickness on the thermal drain and gate current spectral density. It is observed that with the increase in asymmetry in oxide thickness, the drain and gate current spectral density decreases by 10 and 36%, respectively.

Fig. 6 shows the flicker noise drain current spectral density for a sweep of gate voltage  $V_{gs}$  from 0 to 2 V at frequency  $f = 5 \text{ GHz}$  predicted from the proposed model. Since, in TCAD there is no model available for simulating flicker noise, we could not match our results. However, it is observed from the plot that the characteristics as well as the values are coming to be in the same range as predicted from the experimental results for FinFETs [23].

Finally, it is worth noting that mathematically the proposed noise model for asymmetric CDG-MOSFET is very simple in nature and can be easily implemented in a circuit simulator.

### 4 Conclusion

We propose an RF thermal and flicker noise model for CDG-MOSFET based on the linear relationship between surface potentials. The model is adapted to gate-oxide-thickness asymmetry which may prevail in the device due to the process uncertainty. To make the model applicable for short-channel devices we have incorporated the effect of field-dependent mobility in the model. The model is found to be in agreement

with TCAD simulation for varied range of operating conditions and device structures.

## 5 Acknowledgments

This work was supported by the Department of Science and Technology, Government of India, under Ramanna Fellowship grant number: SR/S3/EECE/0123/2011

## 6 References

- 1 Wong, H.S.P.: 'Beyond the conventional transistor', *IBM J. Res. Dev.*, 2003, **46**, (2-3)
- 2 Colinge, J.P.: 'FinFETs and other multi-gate transistors' (Springer-Verlag, 2008)
- 3 Scholten, A.J., Tiemeijer, L.F., Langevelde, R., *et al.*: 'Noise modeling for RF CMOS circuit simulation', *IEEE Trans. Electron Devices*, 2003, **50**, (3), pp. 618–632
- 4 Jayaraman, R., Sodini, C.G.: 'A 1/f noise technique to extract the oxide trap density near the conduction band edge', *IEEE Trans. Electron Devices*, 1989, **36**, (9), pp. 1773–1782
- 5 McWhorter, A.L.: '1/f noise and germanium surface properties' (Semiconductor Surface Physics, University of Pennsylvania Press, Philadelphia, PA, 1957), p. 207
- 6 Hooge, F.N., Kleinpenning, T.G.M., Vandamme, L.K.J.: 'Experimental studies on 1/f noise'. Reports On Progress in Physics., 1981, **44**, pp. 497–532
- 7 Vandamme, E.P., Vandamme, L.K.J.: 'Critical discussion on unified 1/f noise models for MOSFETs', *IEEE Trans. Electron Devices*, 2000, **47**, (11), pp. 2146–2152
- 8 Knoblinger, G., Klein, P., Tiebout, M.: 'A new model for thermal channel noise of deep-submicron MOSFETs and its application in RF-CMOS design', *IEEE J. Solid-State Circuits*, 2001, **36**, (5), pp. 831–837
- 9 Ong, S.N., Chew, K.W.J., Yeo, K.S., *et al.*: 'A new unified model for channel thermal noise of deep sub-micron RFCMOS'. IEEE Int. Symp. on Radio-Frequency Integration Technology, Singapore, 2009, pp. 280–283
- 10 Sharan, N., Mahapatra, S.: 'A short channel common double gate MOSFET model adapted to gate oxide thickness asymmetry', *IEEE Trans. Electron Devices*, 2014, **61**, (8), pp. 2732–2737
- 11 'ATLAS, Device simulation framework, Version 5.18.3.R', Users' Manual [online] (Silvaco International, USA, 2012). Available at: [www.silvaco.com](http://www.silvaco.com)
- 12 Sallese, J.M., Krummenacher, F., Pregaldiny, F., *et al.*: 'A design oriented charge-based current model for symmetric DG MOSFET and its correlation with the EKV formalism', *Solid State Electron.*, 2005, **49**, (11), pp. 485–489
- 13 Dessai, G., Dey, A., Gildenblat, G., *et al.*: 'Symmetric linearization method for double-gate and surrounding-gate MOSFET models', *Solid State Electron.*, 2009, **53**, (11), pp. 548–556
- 14 Lin, C-H., Dunga, M.V., Niknejad, A.M., *et al.*: 'A compact quantum-mechanical model for double-gate MOSFET'. Solid-State and Integrated Circuits Conf. (ICSICT), Shanghai, 2006, pp. 1272–1274
- 15 Lime, F., Iniguez, B., Moldovan, O.: 'A quasi-two-dimensional compact drain-current model for undoped symmetric double-gate MOSFETs including short channel effects', *IEEE Trans. Electron Devices*, 2008, **55**, (6), pp. 1441–1448
- 16 Yamaguchi, K.: 'Field-dependent mobility model for two-dimensional numerical analysis of MOSFETs', *IEEE Trans. Electron Devices*, 1979, **26**, (7), pp. 1068–1074
- 17 Langevelde, R.V., Paasschens, J.C.J., Scholten, A.J., *et al.*: 'New compact model for induced gate current noise', Int. Electron Devices Meet. *IEDM*, USA, 2003, pp. 36.2.1–36.2.4
- 18 Jandhyala, S., Kashyap, R., Anghel, C., *et al.*: 'A simple charge model for symmetric double-gate MOSFETs adapted to gate-oxide-thickness asymmetry', *IEEE Trans. Electron Devices*, 2012, **59**, (4), pp. 1002–1007
- 19 Lazaro, A., Cerdeira, A., Nac, B., *et al.*: 'High-frequency compact analytical noise model for double-gate metal–oxide–semiconductor field-effect transistor', *J. Appl. Phys.*, 2009, **105**, pp. 034510-01–034510-09
- 20 Hung, K.K., Ko, P.K., Hu, C., *et al.*: 'A unified model for the flicker noise in metal–oxide–semiconductor field-effect transistors', *IEEE Trans. Electron Devices*, 1990, **37**, (3), pp. 654–665
- 21 Reimbold, G.: 'Modified 1/f trapping noise theory and experiments in MOS transistors biased from weak to strong inversion- influence of interface states', *IEEE Trans. Electron Devices*, 1984, **31**, (9), pp. 1190–1198
- 22 Li, Z., Ma, J., Ye, Y., *et al.*: 'Compact channel noise models for deep-submicron MOSFETs', *IEEE Trans. Electron Devices*, 2009, **56**, (6), pp. 1300–1308
- 23 Wei, C., Xiong, Y-Z., Zhou, X.: 'Investigation of low-frequency noise in N-channel FinFETs from weak to strong inversion', *IEEE Trans. Electron Devices*, 2009, **56**, (11), pp. 2800–2810